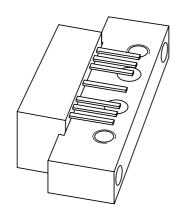
DISCRETE SEMICONDUCTORS

DATA SHEET



BGY67 200 MHz, 22 dB gain reverse amplifier

Product specification Supersedes data of 1997 Apr 15 2001 Oct 18





200 MHz, 22 dB gain reverse amplifier

BGY67

FEATURES

- · Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- TiPtAu metallized crystals ensure optimal reliability.

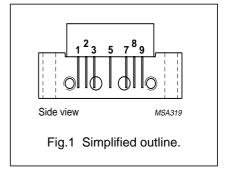
DESCRIPTION

Hybrid amplifier module for CATV systems operating over a frequency range of 5 to 200 MHz at a voltage supply of +24 V (DC). The device is intended as a reverse amplifier for use in two way systems.

PINNING - SOT115J

PIN	DESCRIPTION	
1	input	
2	common	
3	common	
5	+V _B	
7	common	
8	common	
9	output	

PIN CONFIGURATION



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G _p	power gain	f = 10 MHz	21.5	_	22.5	dB
I _{tot}	total current consumption (DC)	V _B = +24 V	_	215	230	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER		MAX.	UNIT
Vi	RF input voltage		65	dBmV
T _{stg}	storage temperature		+100	°C
T _{mb}	mounting base operating temperature		+90	°C

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CHARACTERISTICS

Table 1 Bandwidth 5 to 200 MHz; $T_{mb} = 30 \, ^{\circ}\text{C}$; $Z_S = Z_L = 75 \, \Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gp	power gain	f = 10 MHz	21.5	_	22.5	dB
SL	slope cable equivalent	f = 5 to 200 MHz	-0.2	_	+0.5	dB
FL	flatness of frequency response	f = 5 to 200 MHz	_	_	±0.2	dB
S ₁₁	input return losses	f = 5 to 200 MHz	20	_	_	dB
S ₂₂	output return losses	f = 5 to 200 MHz	20	_	_	dB
СТВ	composite triple beat	22 channels flat; V _o = 50 dBmV; measured at 175.25 MHz	_	_	-67	dB
X _{mod}	cross modulation	22 channels flat; V _o = 50 dBmV; measured at 55.25 MHz	_	_	-60	dB
d ₂	second order distortion	V _o = 50 dBmV; note 1	_	_	-67	dB
Vo	output voltage	d _{im} = −60 dB; note 2	67	_	_	dBmV
		d _{im} = −60 dB; note 3	64	_	_	dBmV
F	noise figure	f = 200 MHz	_	_	5.5	dB
I _{tot}	total current consumption	DC value; V _B = +24 V; note 4	_	215	230	mA

Notes

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1. f_p = 83.25 MHz; V_p = 50 dBmV; f_q = 109.25 MHz; V_q = 50 dBmV; measured at f_p + f_q = 192.5 MHz.
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2. Measured according to DIN45004B;

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\begin{split} f_p &= 35.25 \text{ MHz; } V_o = V_p; \\ f_q &= 42.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 44.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 33.25 \text{ MHz.} \end{split}
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3. Measured according to DIN45004B;

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\begin{split} f_p &= 187.25 \text{ MHz; } V_o = V_p; \\ f_q &= 194.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 196.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 185.25 \text{ MHz.} \end{split}
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4. The module normally operates at $V_B = +24 \text{ V}$, but is able to withstand supply transients up to +30 V.

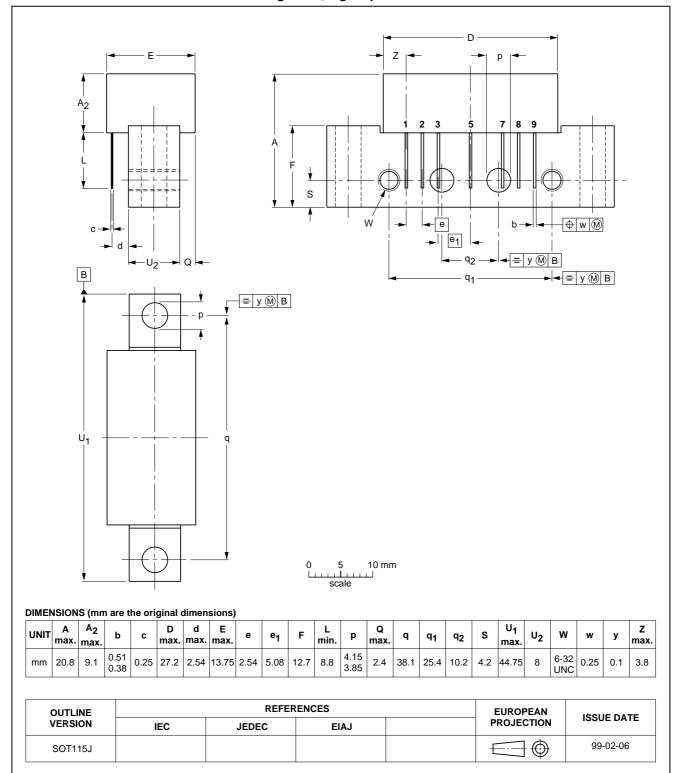
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PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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NOTES

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NOTES

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